

FIG. 1

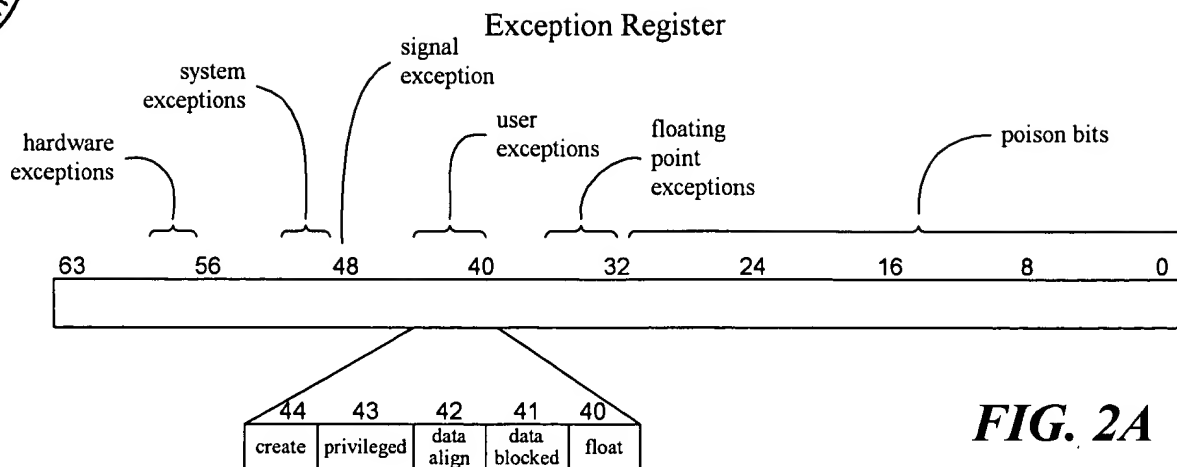


FIG. 2A

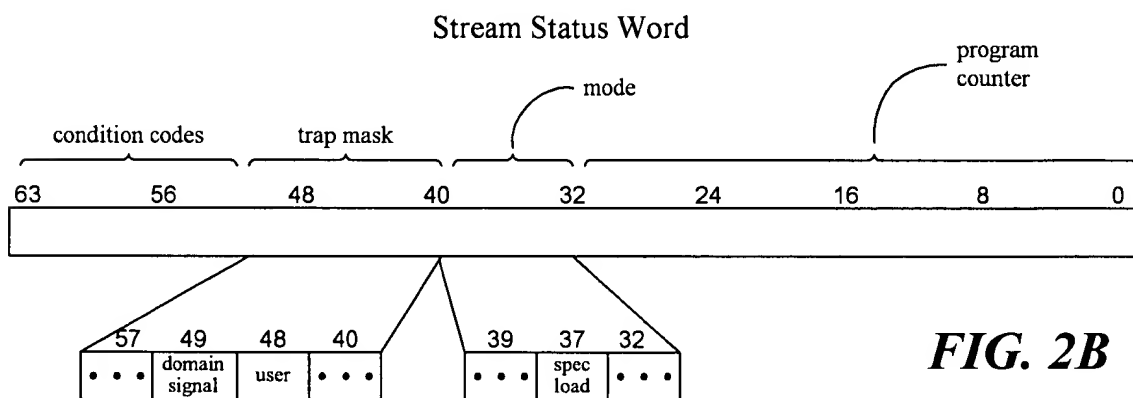


FIG. 2B

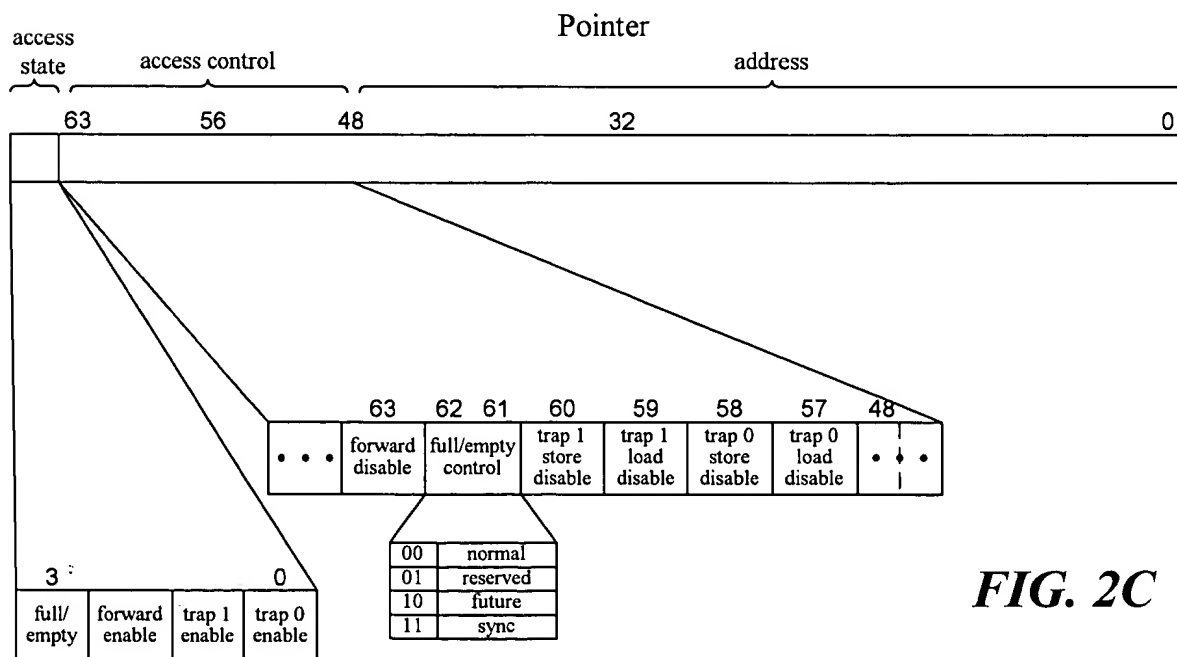


FIG. 2C

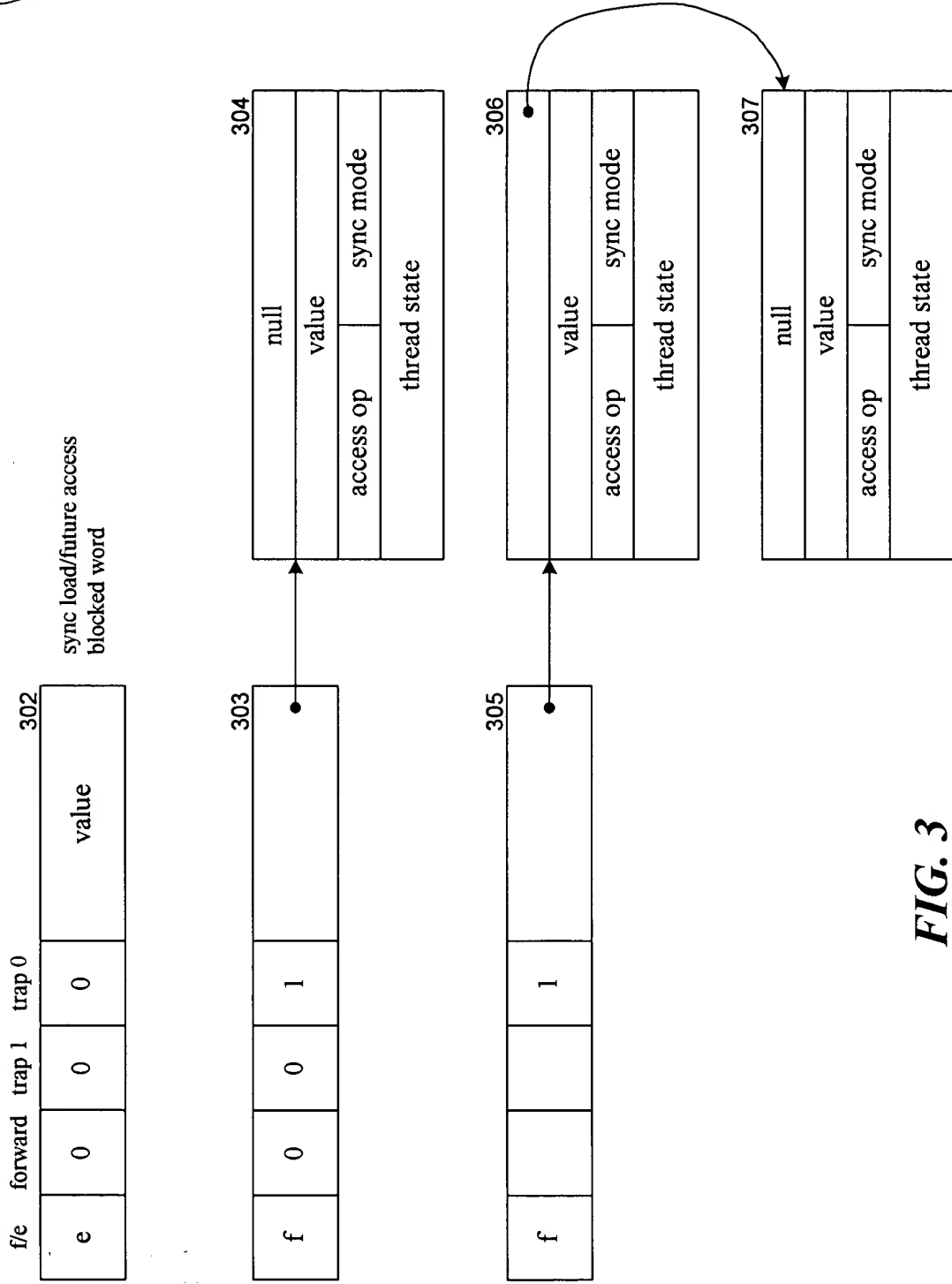


FIG. 3



401

<div>sync mode current f/e</div>	load			store		
	normal	sync	future	normal	sync	future
full	succeed	succeed	succeed	succeed	block	succeed
	full	empty *	full	full	N/A	full
empty	succeed	block	block	succeed	succeed	block
	empty	N/A	N/A	full *	full *	N/A

FIG. 4

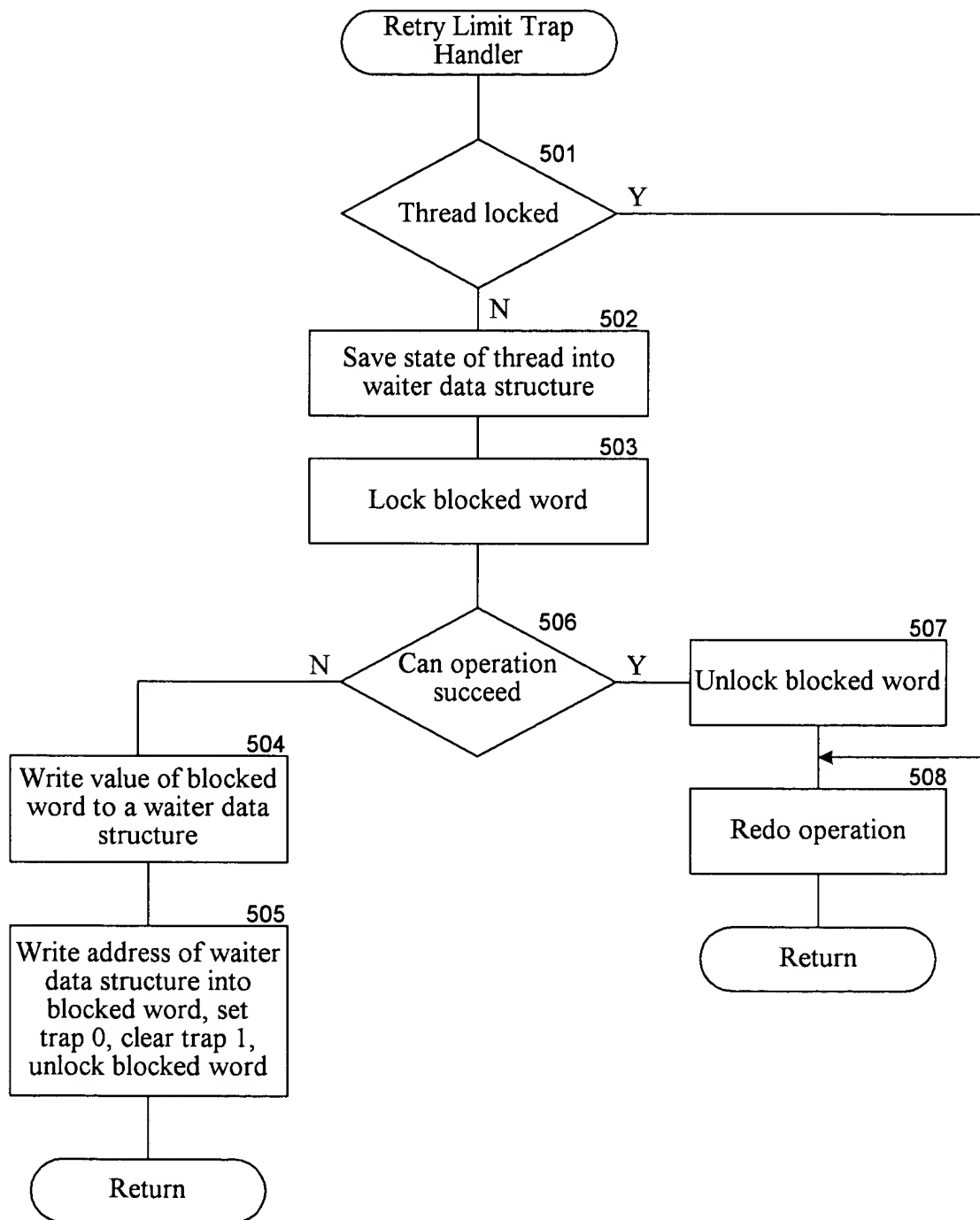


FIG. 5

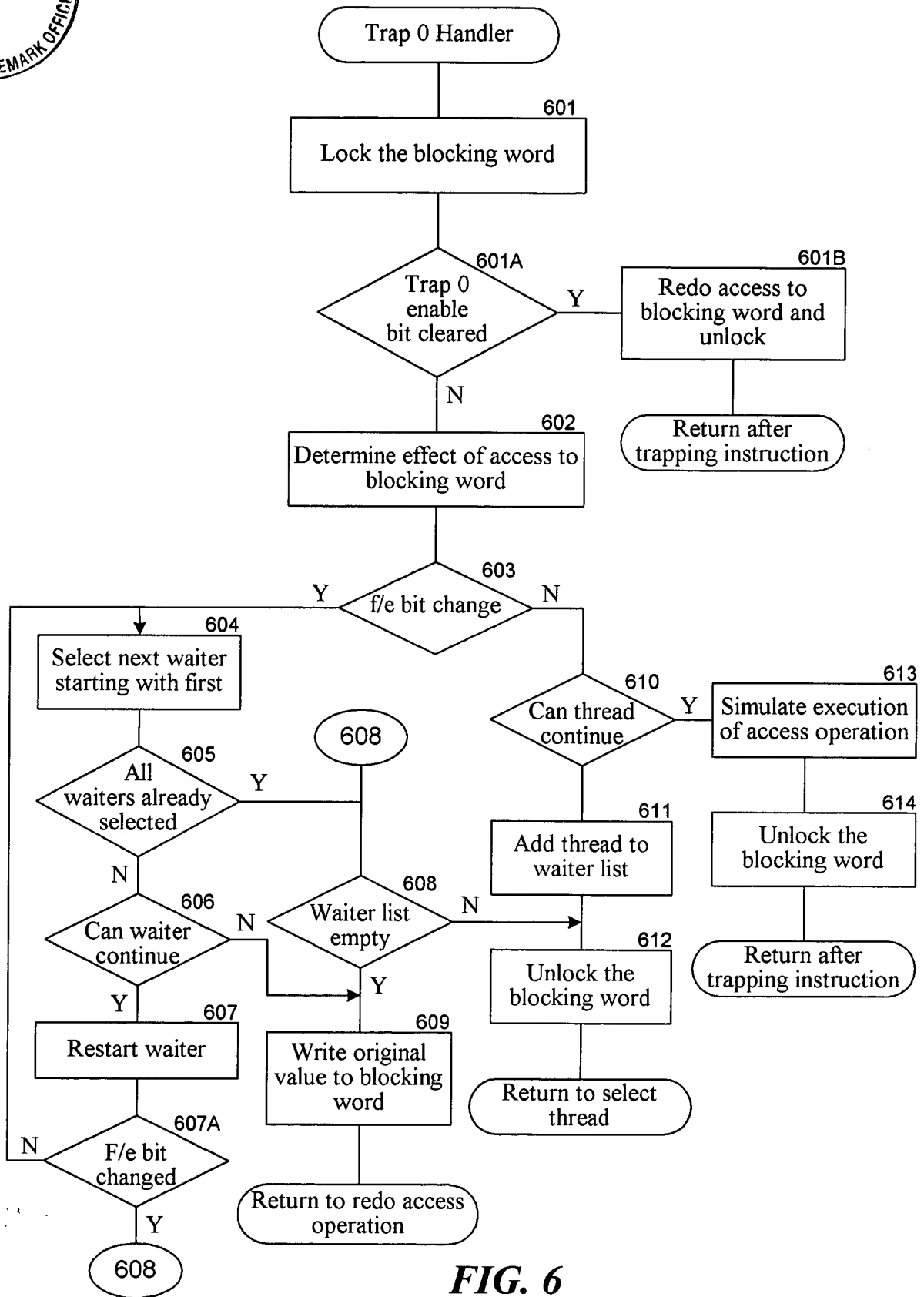


FIG. 6

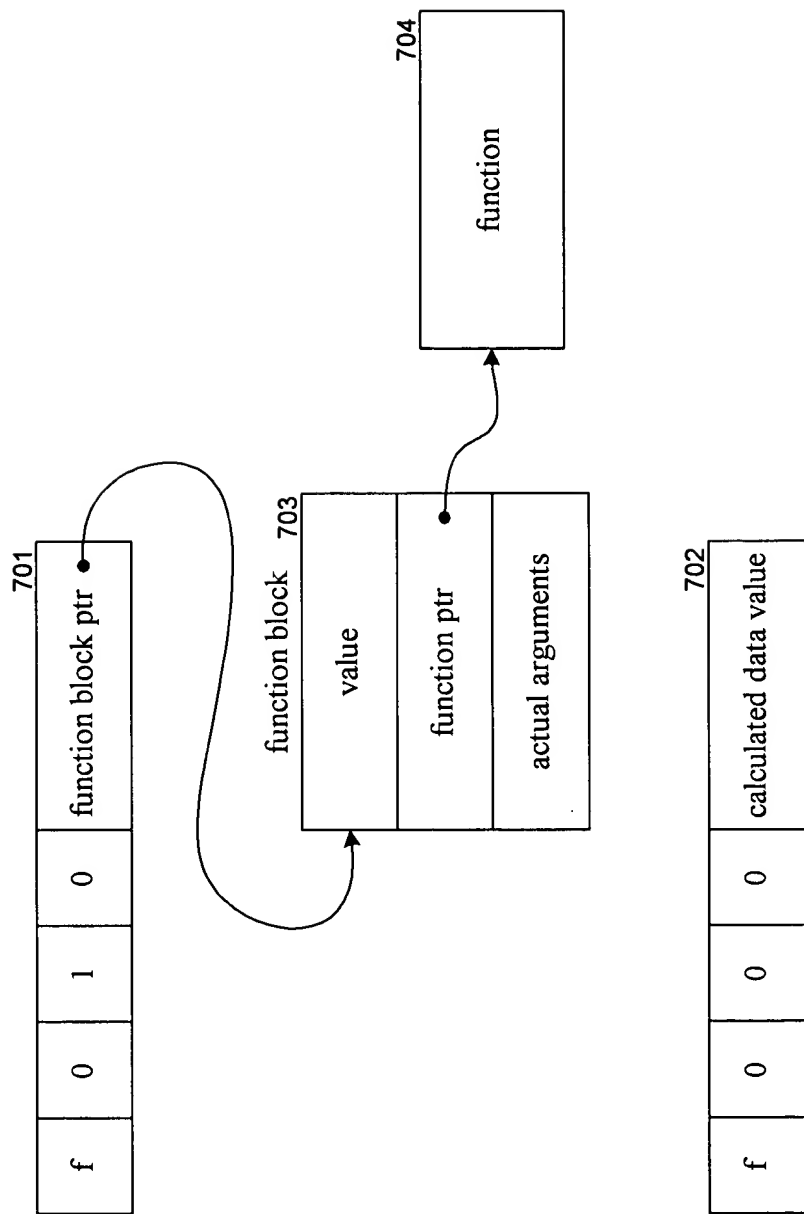


FIG. 7

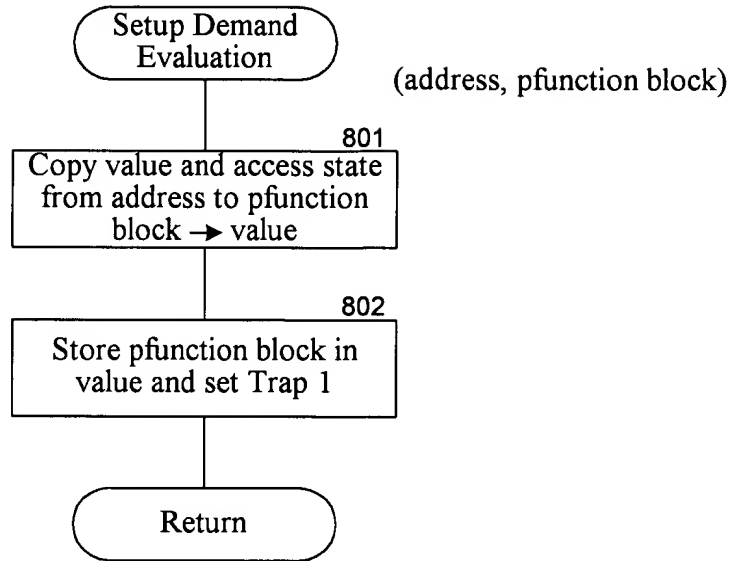


FIG. 8

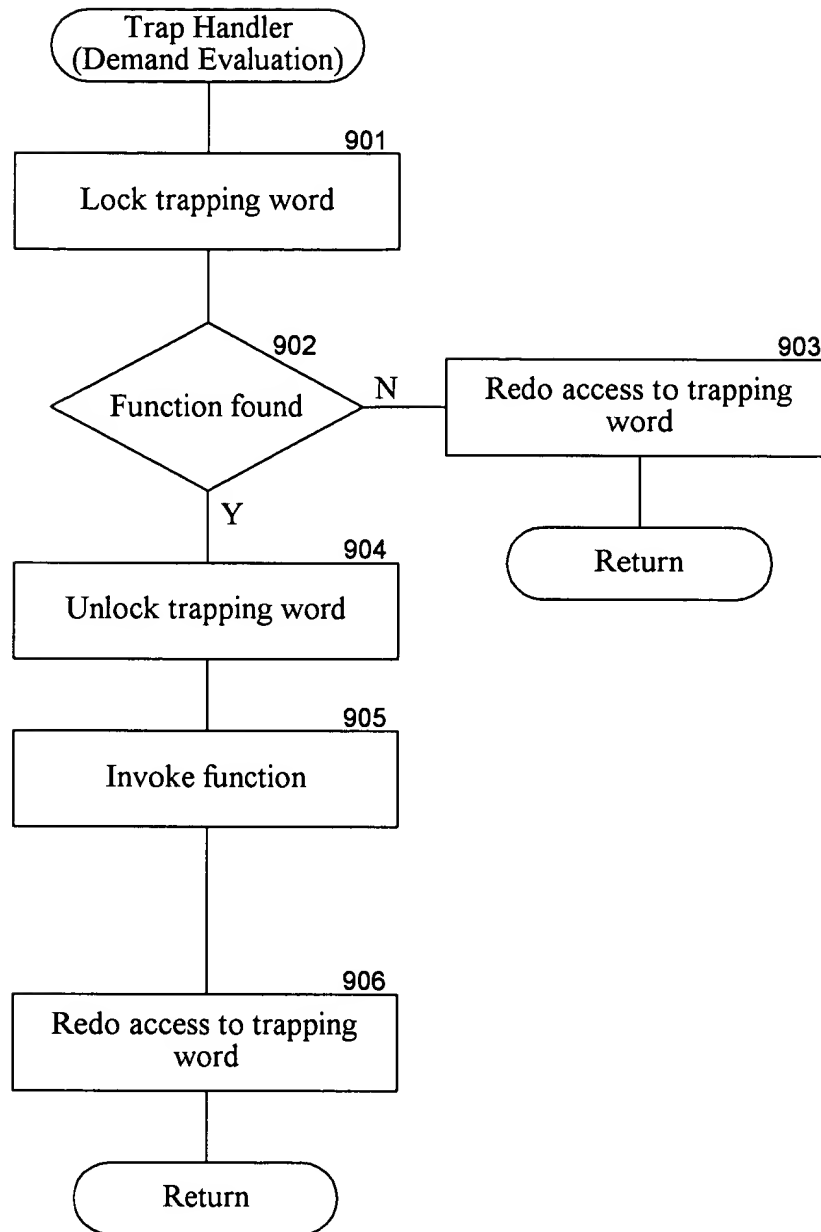


FIG. 9

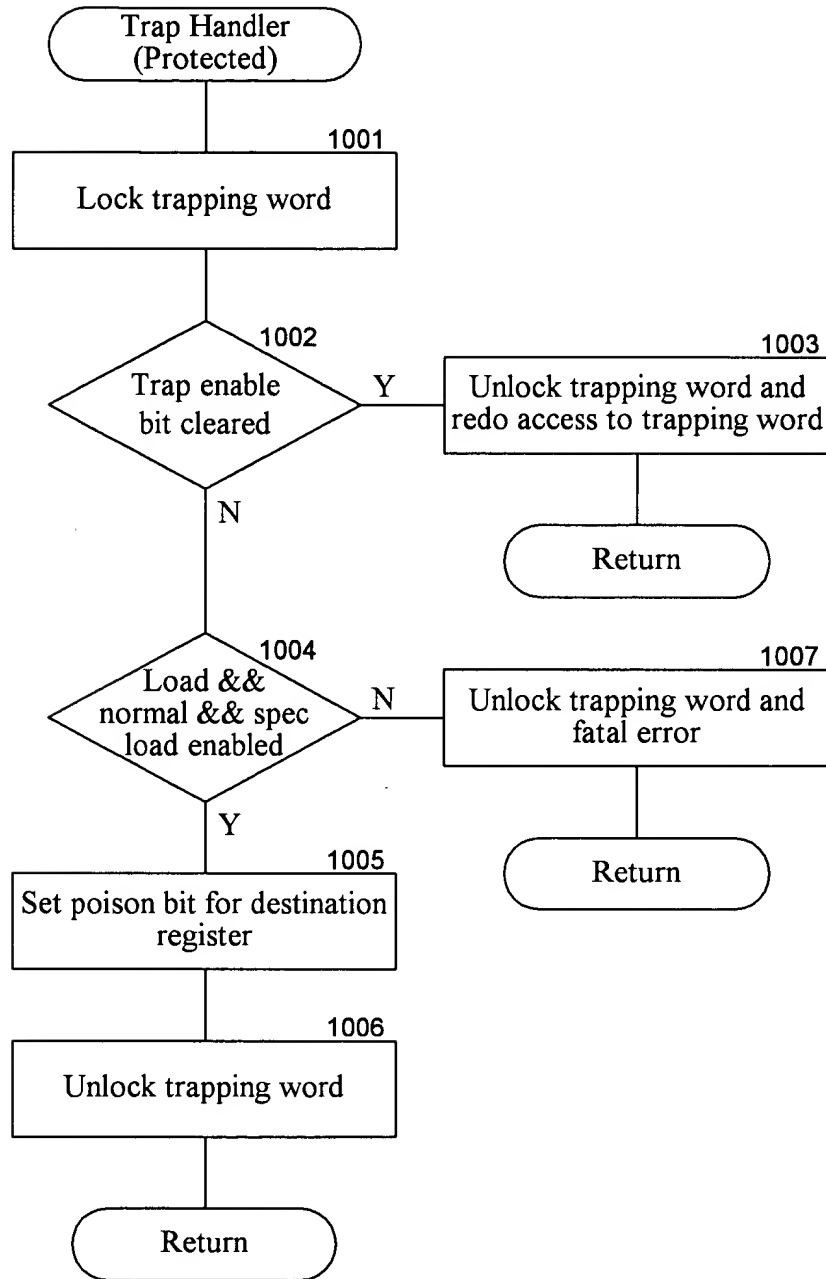


FIG. 10

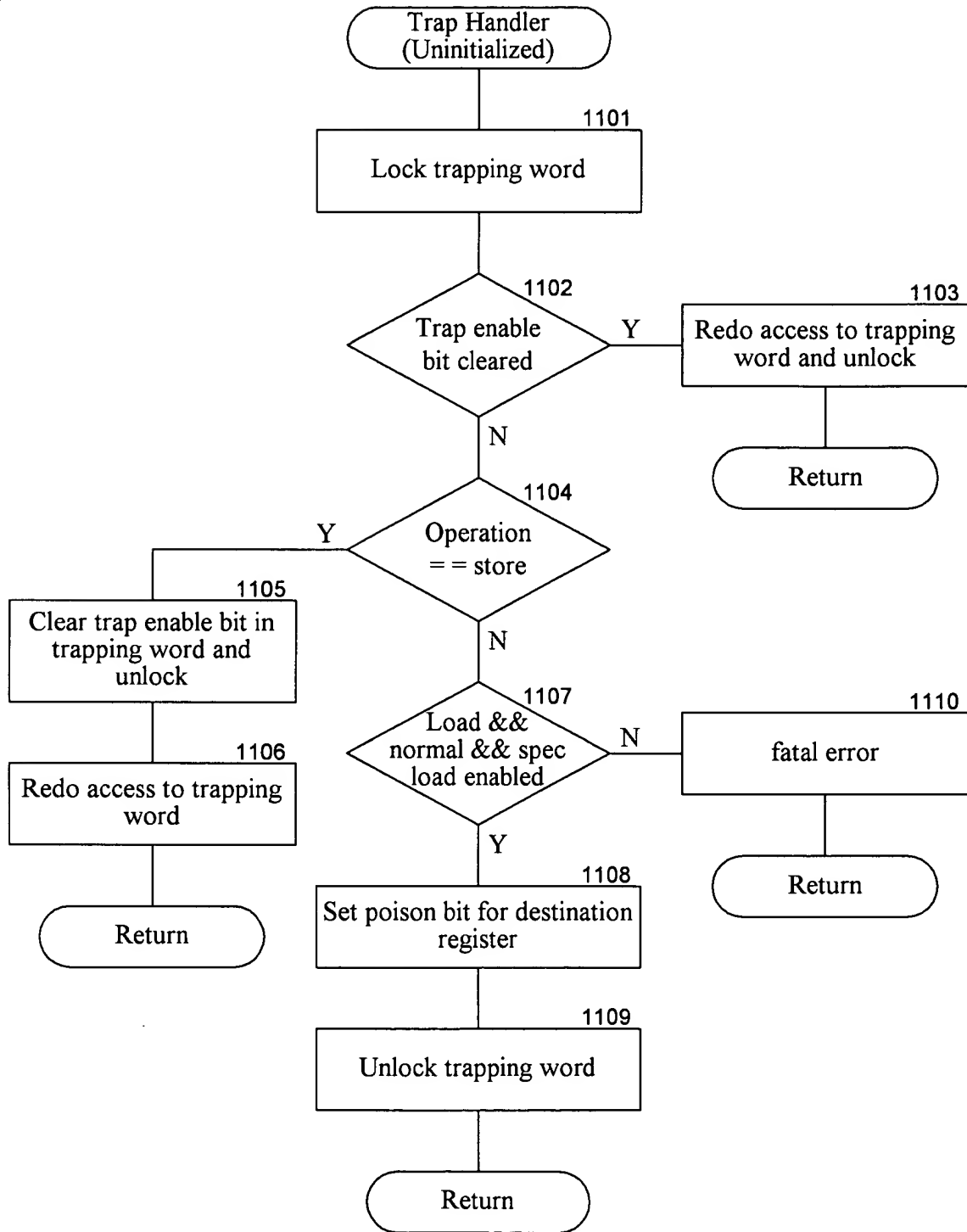


FIG. 11

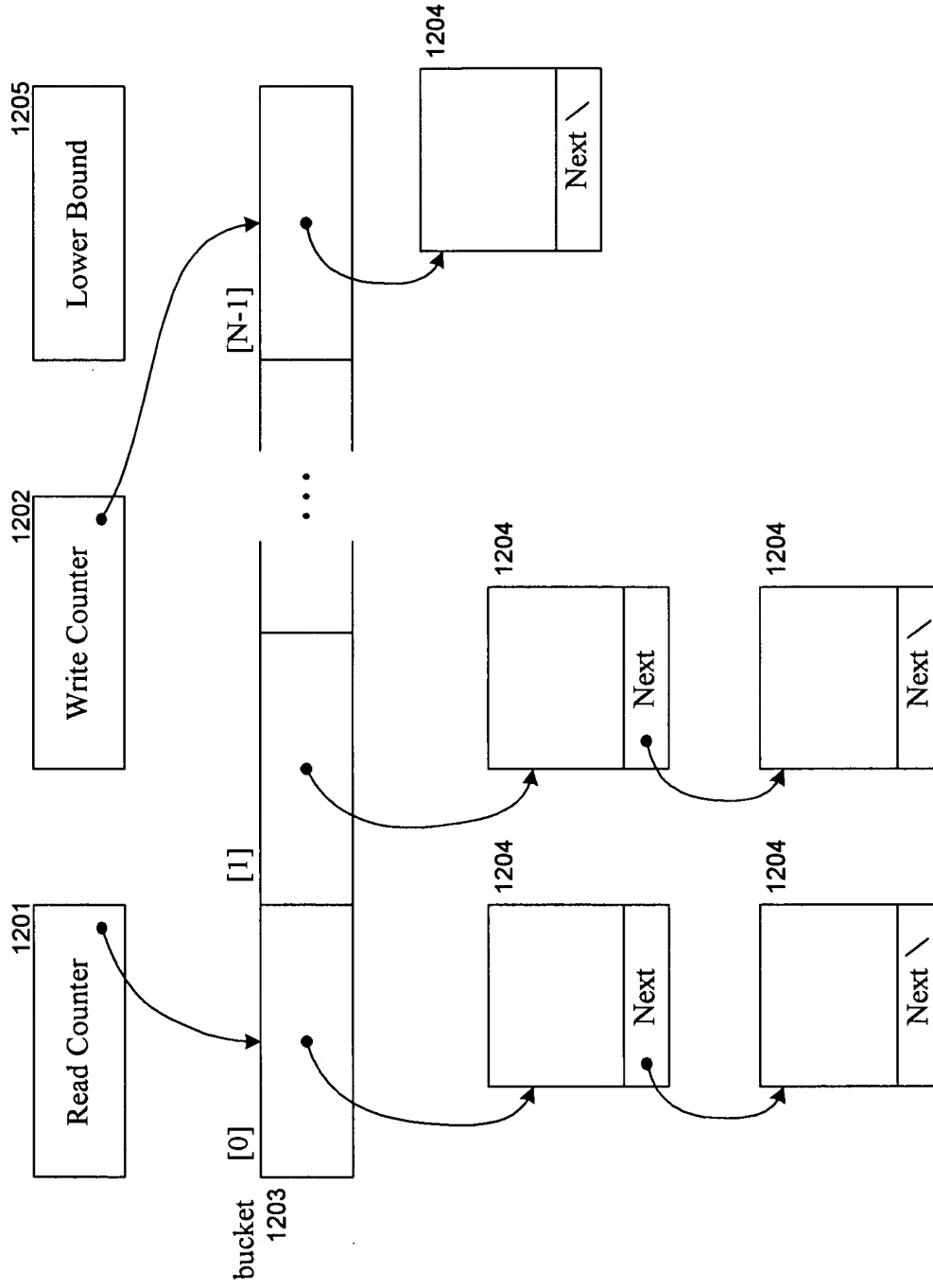


FIG. 12

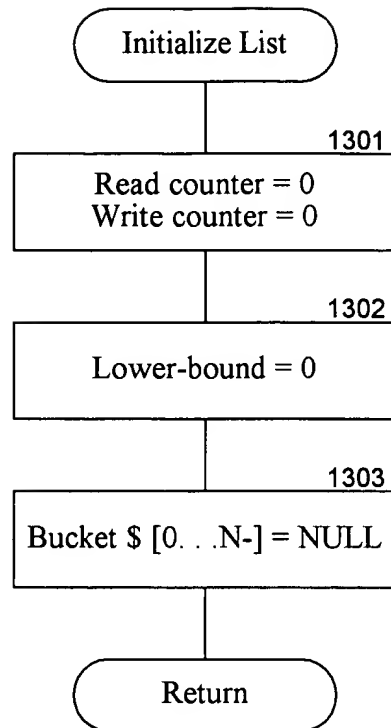


FIG. 13

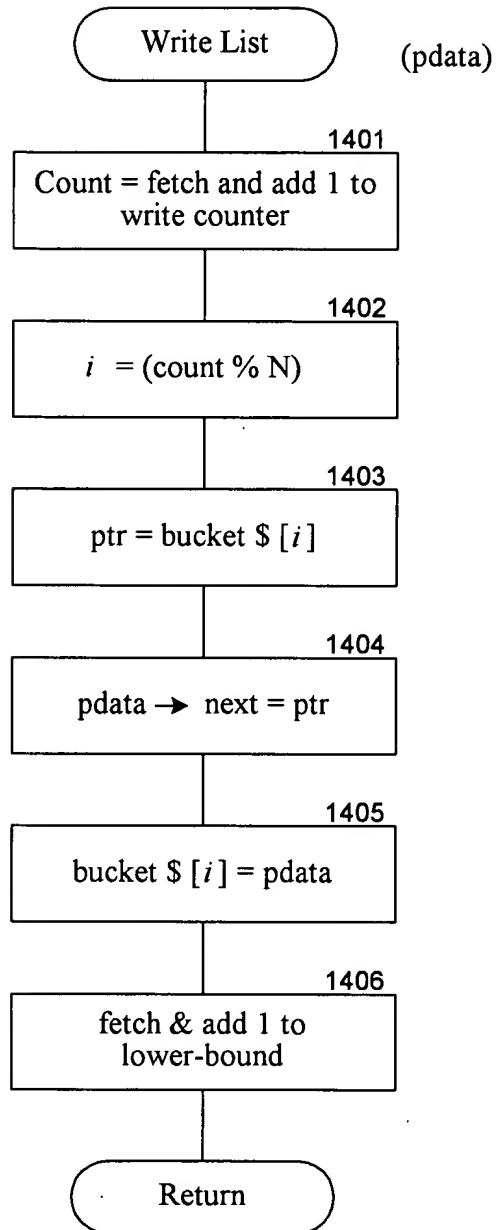


FIG. 14

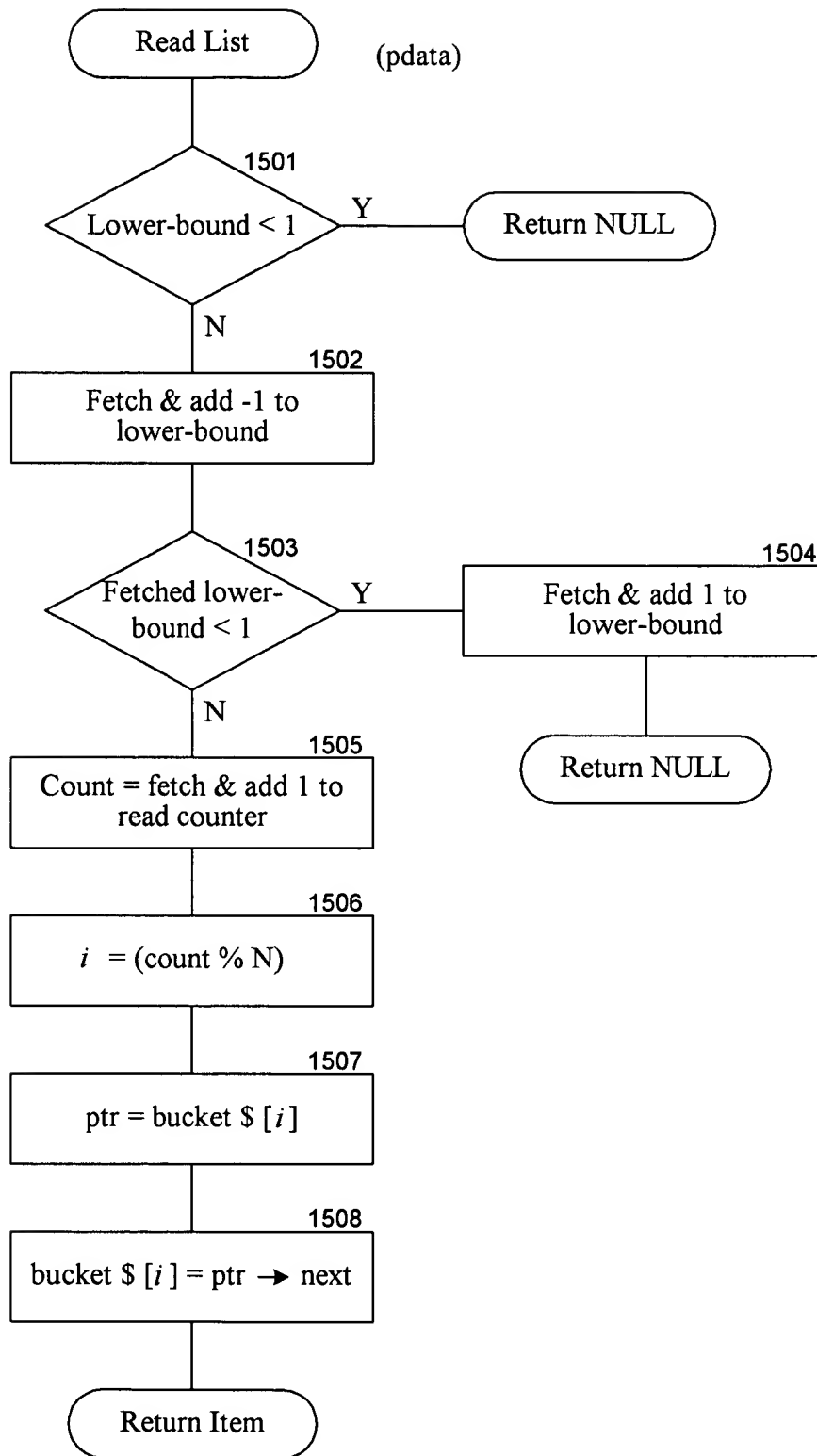


FIG. 15

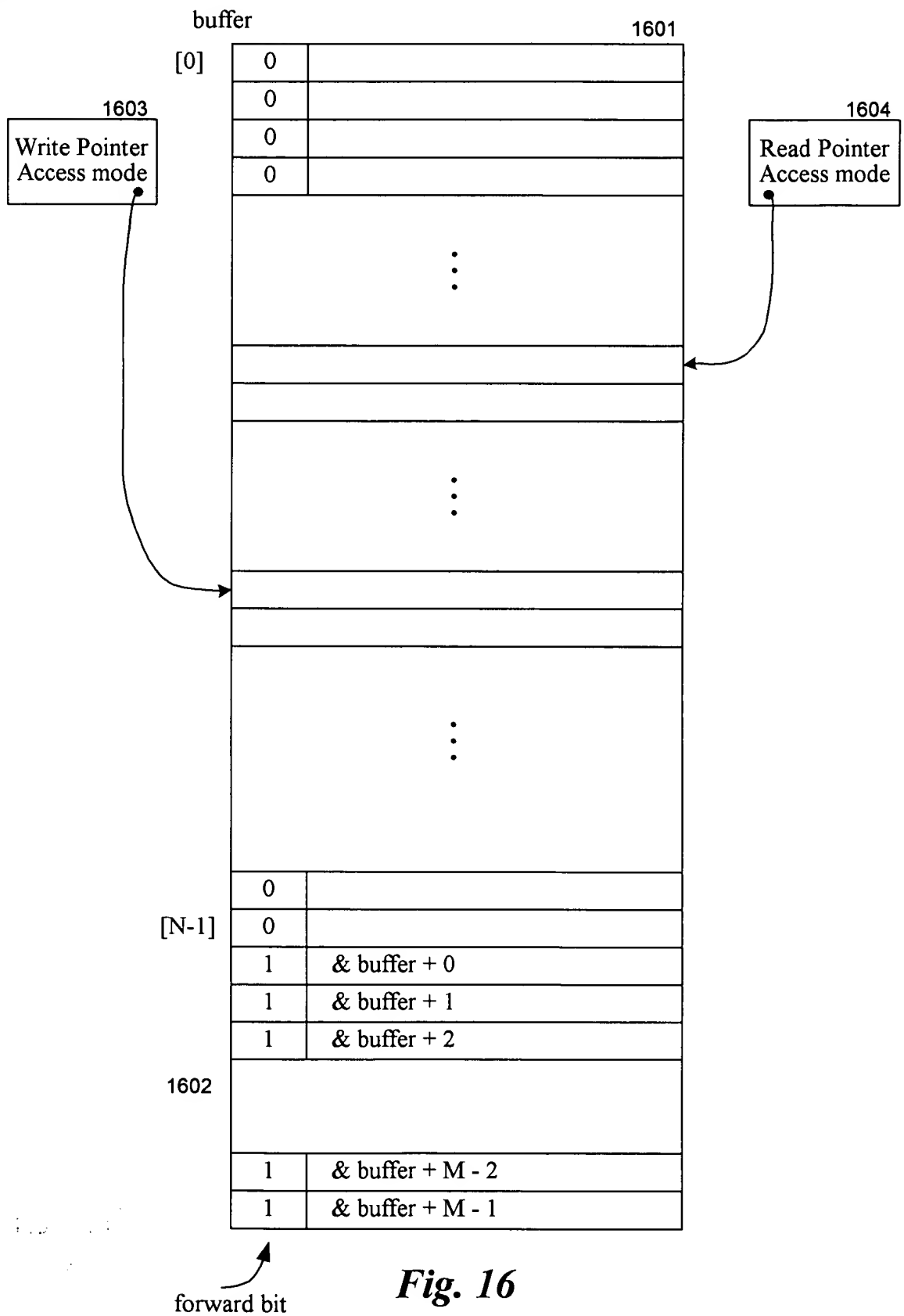


Fig. 16

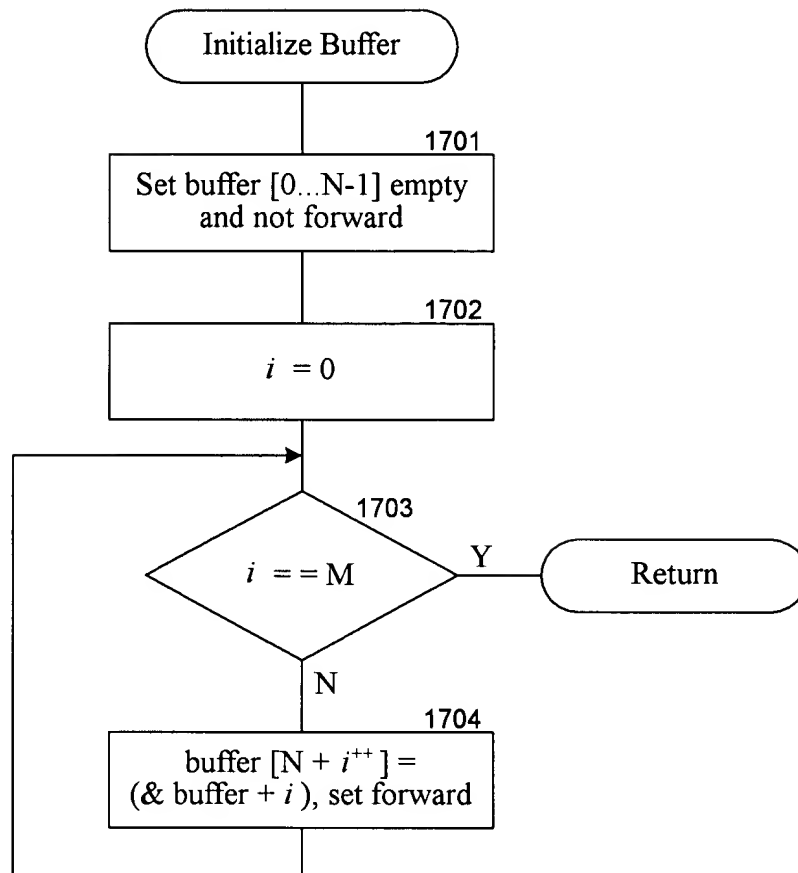


FIG. 17

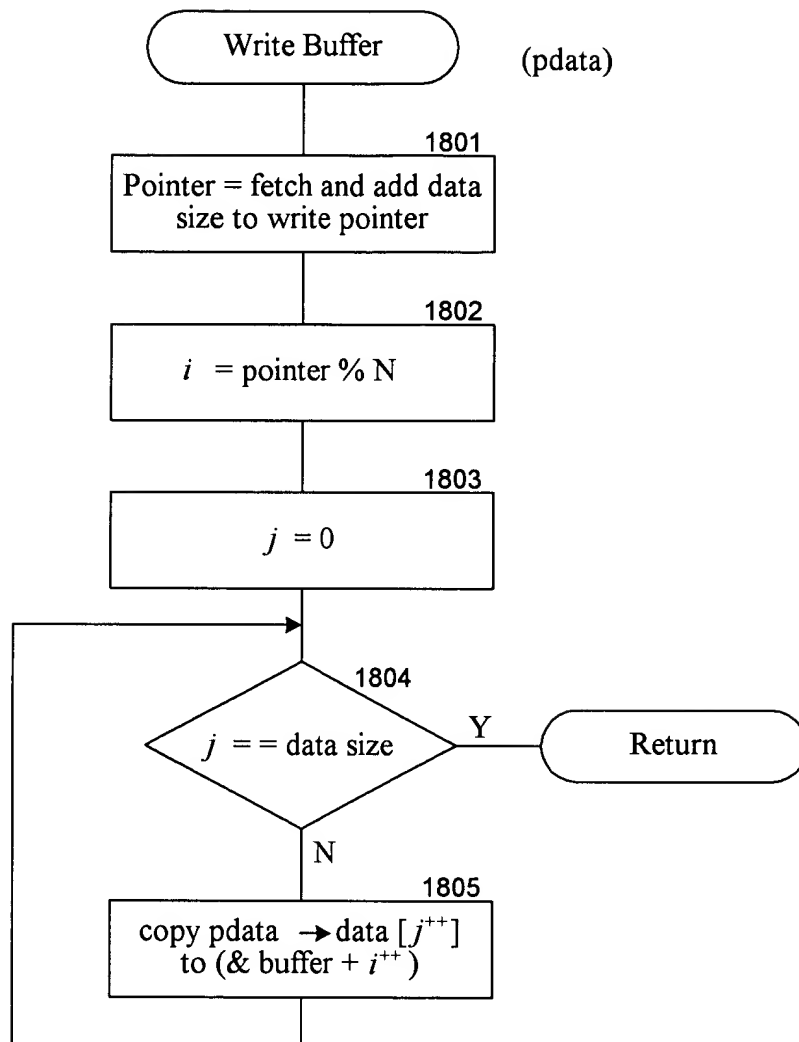


FIG. 18

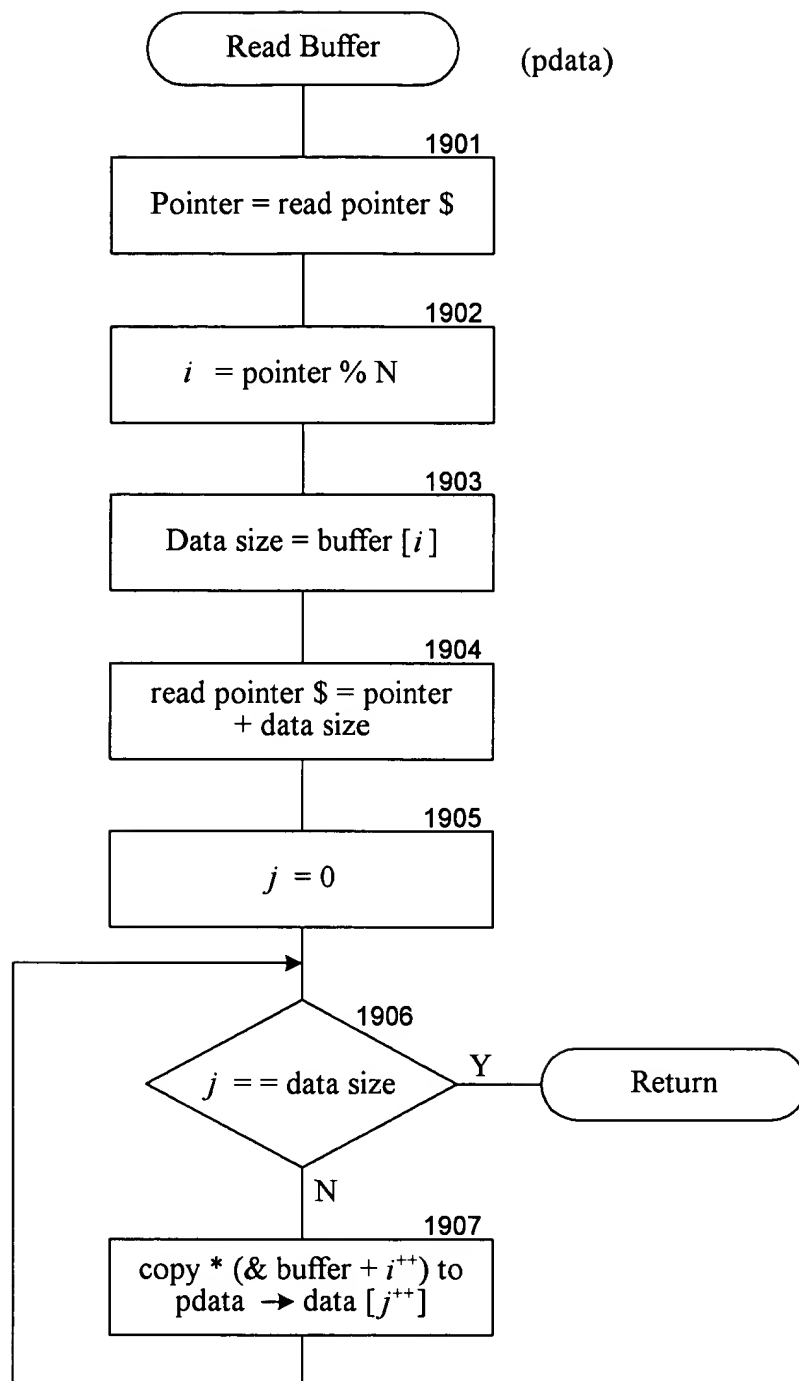
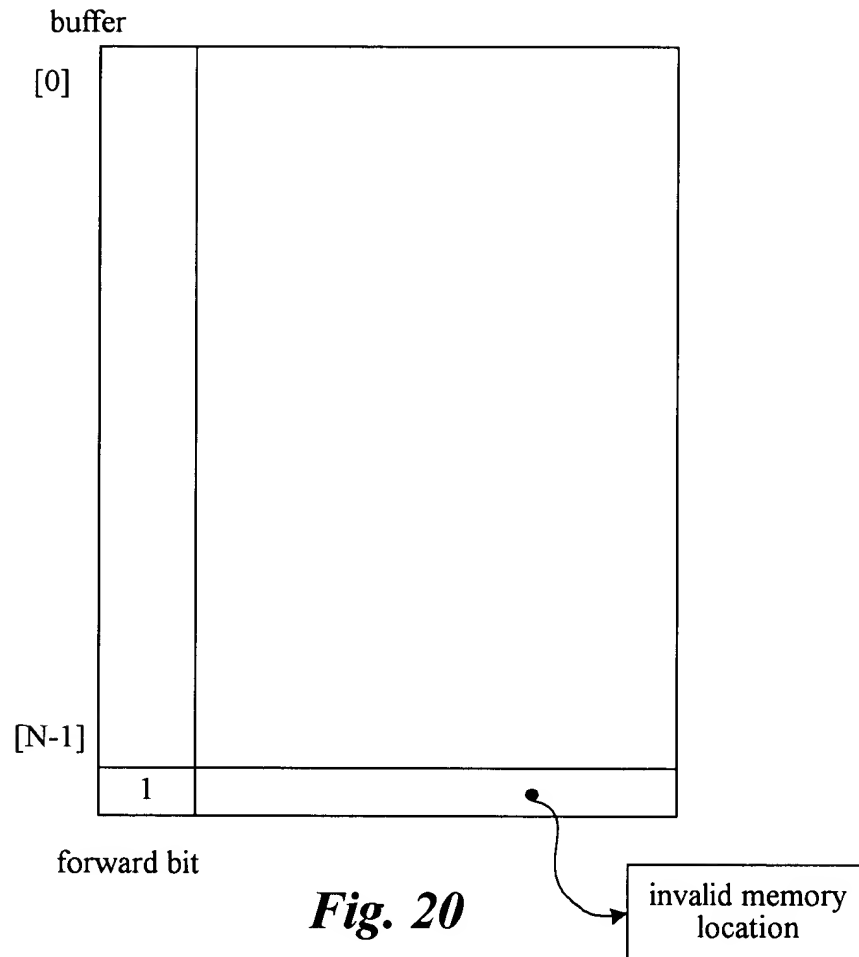


FIG. 19



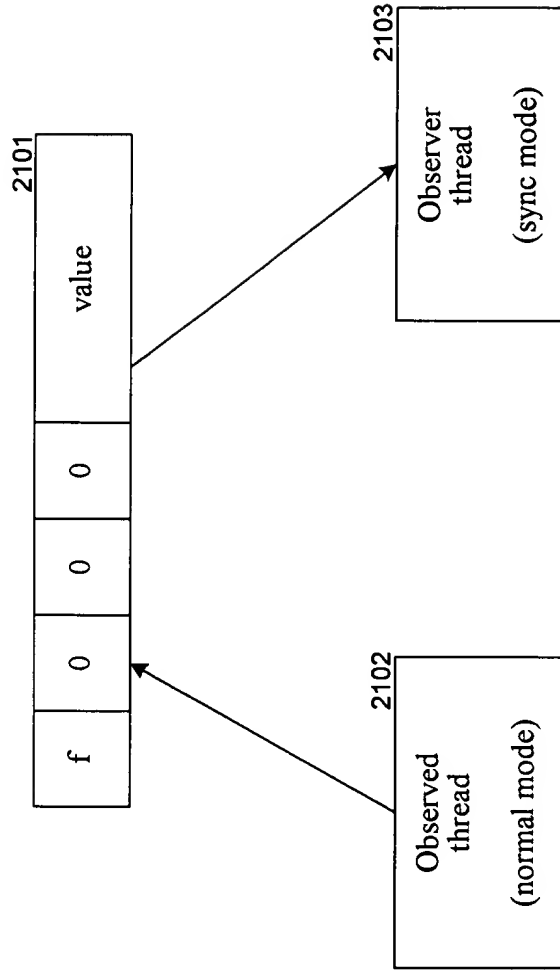


FIG. 21

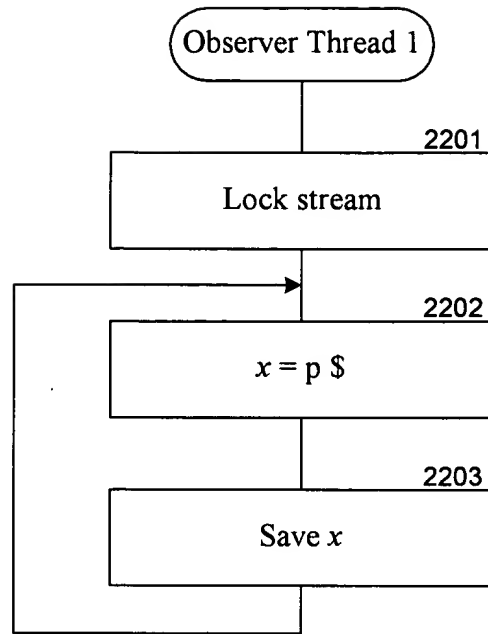


FIG. 22

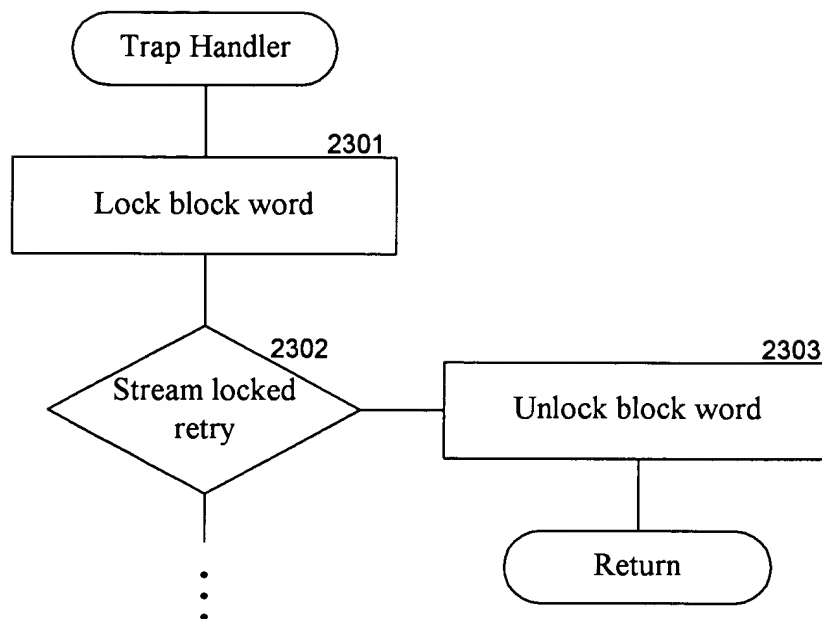


FIG. 23

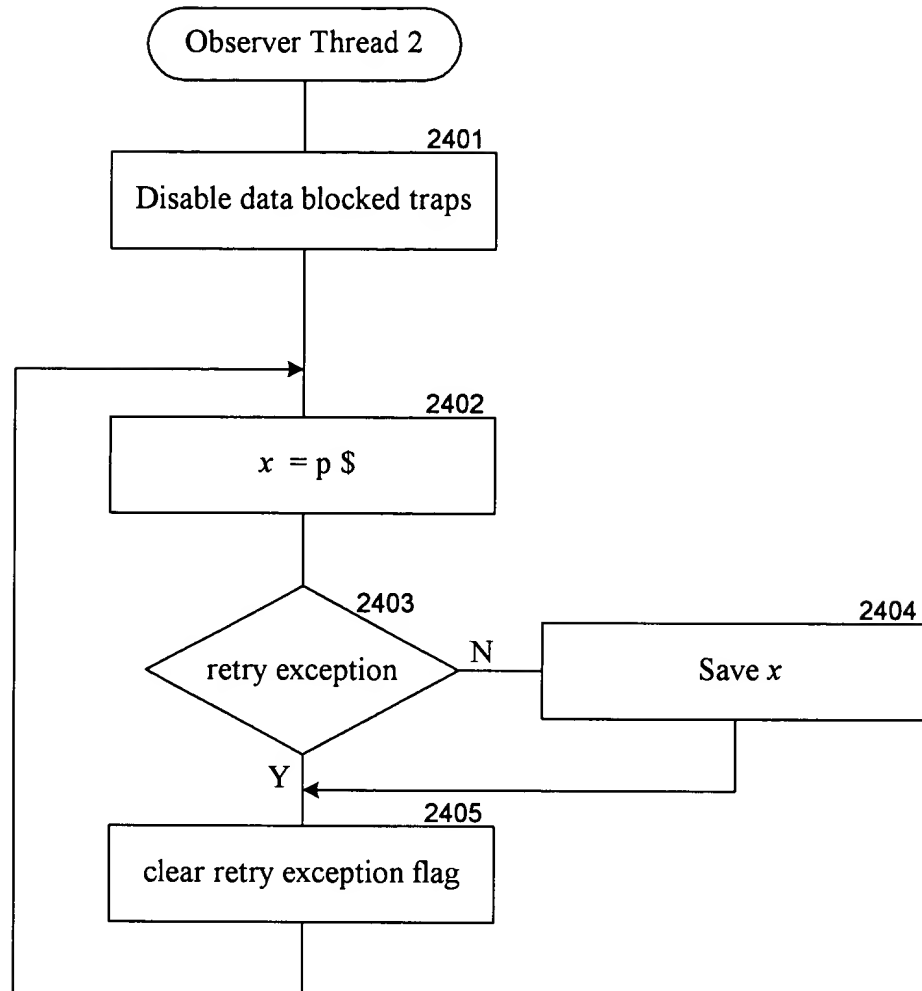


FIG. 24



Data Structure

1	
1	
⋮	⋮
1	
1	

Trap 1

Fig. 25